

FIG. 1

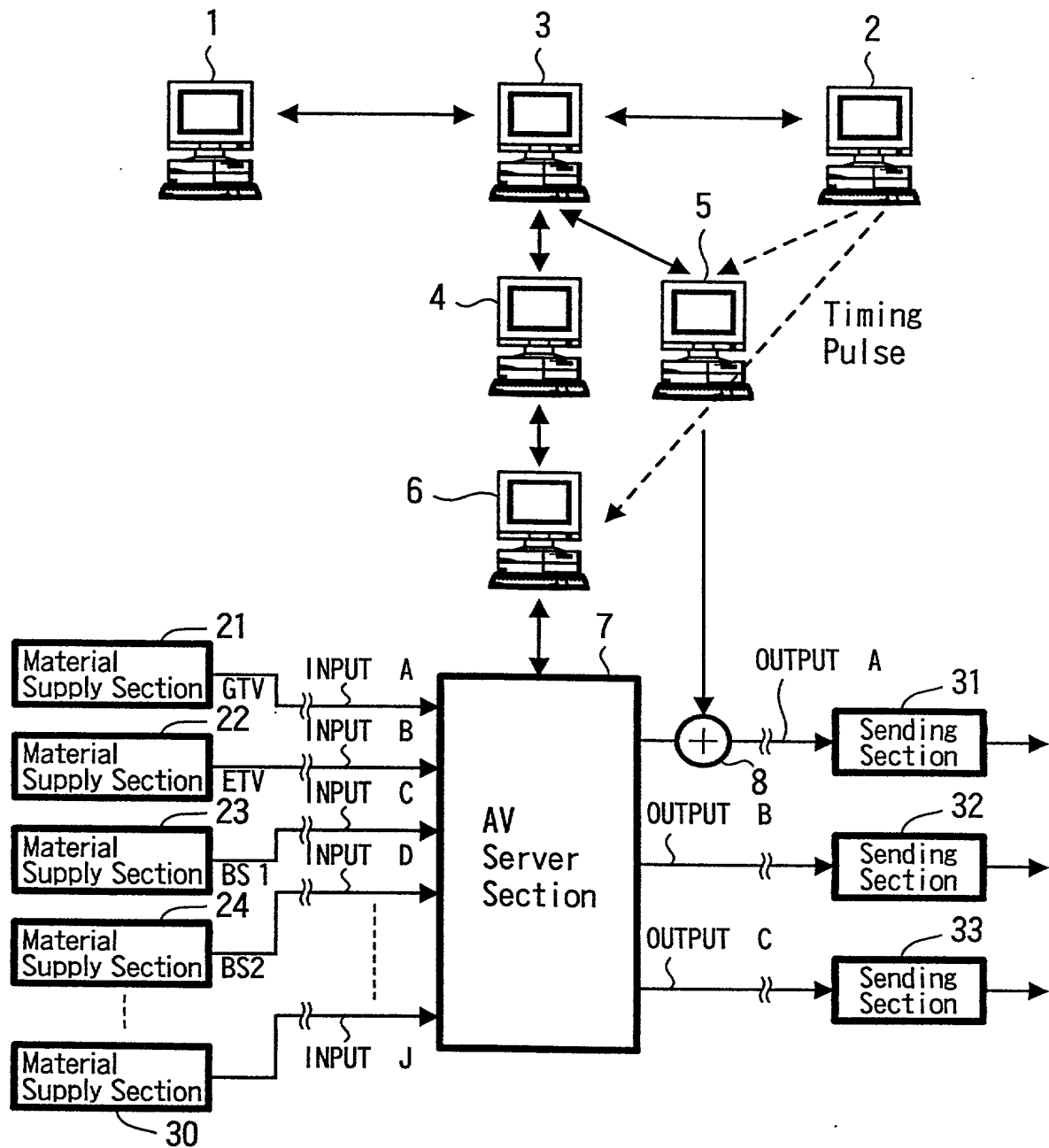




FIG. 3

510

512 511 512 511 Icon

Details of Server Material Data

Program Code

0000000

Sound Version

CH1

J

CH2

Editing Version

0

Program Name

Subtitle

Recording Date

1997/01/31

Start Time

14:02:00

End Time

14:23:00

Recording

00:01:00

STC:

RefTC

Preset

(00:00:00:00)

Material Length

00:00:03:00

SOM Point

00:00:03:00

EOM Point

00:00:03:00

Recording

B31

Resource

Sound Mode

ch1&ch2

ch3&ch4

Recording Method

Manual

Automatic

Time Differential

Time Differential

Flag

No Time Differential

Editing

Necessary

Unnecessary

Completed

Sending Date

/ /

Start Time

:

End Time

:

Broadcast

Material Length

Remarks

Scheduled

1999/12/31 23:00

Deletion Date

Material File Name

MAIN A VOL1:1234567

SUB A VOL5:0987654

B VOL2:2345678(Error)

Detailed Contents

of Error

Update

Close

FIG. 4

Details of Sending Data			
Program Code	0000000	Sound Version	CH1 J CH2 [v] Editing Version 0
Program Name			
Subtitle			
Sending Date	1998/09/11	Start Time	::
Sending Length	00:01:05	Material Length	00:01:10
SOM Point	00:00:00:00	EOM Point	00:01:05:00
Sound Mode	ch1&ch2	ch3&ch4	[v]
Time Differential	No Time Differential		
Editing Flag	Unnecessary		
Scheduled Deletion Date	1999/12/31		
Material File Name	MAIN A VOL1:1234567		B VOL2:2345678(Error)
	SUB A VOL5:0987654		B VOL9:9876543
Detailed Contents of Error			
<div>Insert</div> <div>Close</div>			

## Ready

FIG. 6

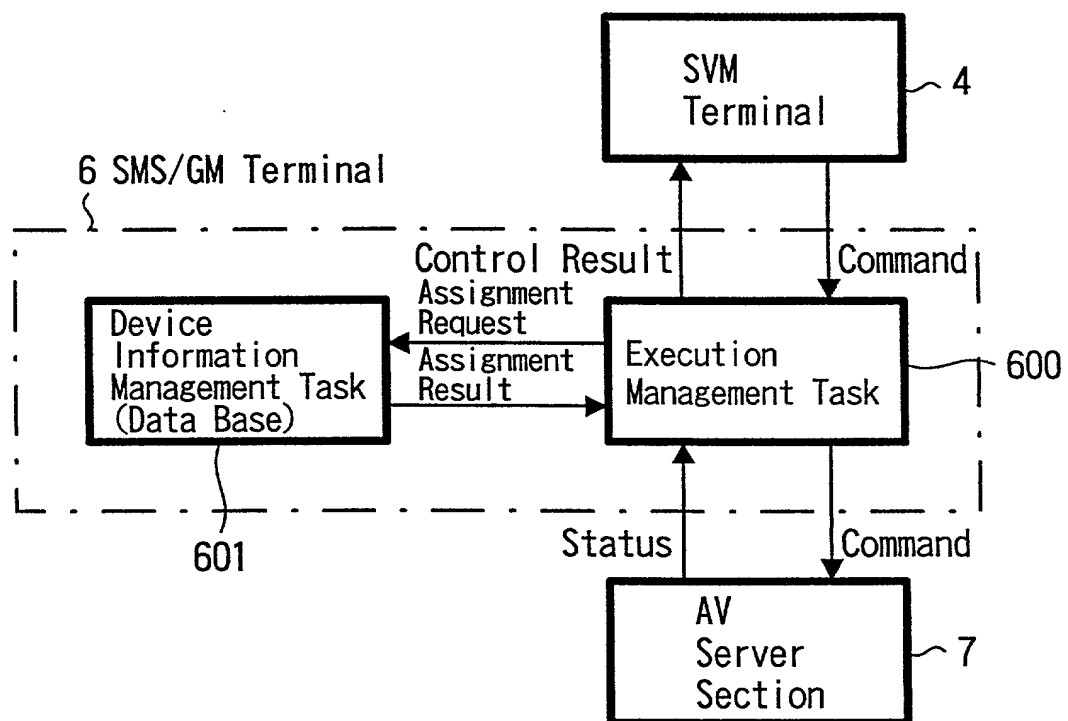


FIG. 7

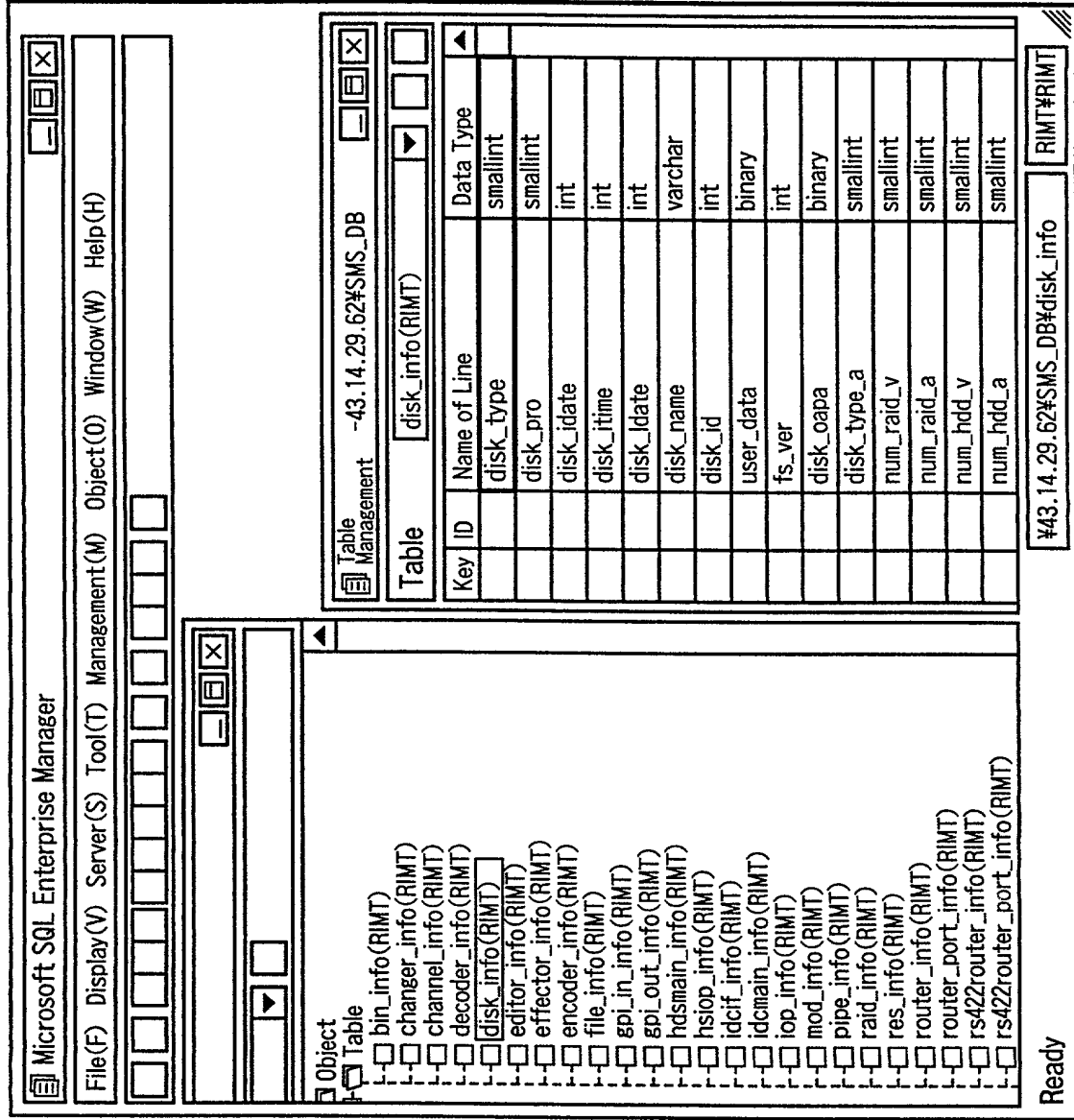


FIG. 8A

(RESOURCE SMS)				
#Res Name	Res Type(*.SMS)	Res ID	Gp Name	Init Status
SMS_1	SMS	1	GP_MTPSER	ENABLE
IDC_1	IDCMAIN	2	GP_IDC	ENABLE
; (Omission)				

FIG. 8B

(SMS File)				
- Example of IDCMAIN.SMS				
#Res id	Dev id	IDC Type	Distance(Near/Far)	
2	1.1.0.0	ROOT	NEAR	
; (Omission)				
- Example of IDCIF.SMS				
#Res id	Dev id	Res Name	IF id	If Type
10	1.3.1.0	IDC_3	1	GPI
; (Omission)				
; (Omission)				



## FIG. 9

```
siu-80
  root idc(1){
    builtin AFS(1){
      timer      triggerOut(1) {AUT=30;}
      userActive triggerOut(5) {AUT=29;}
    }
    (Omission)
  }
  sub idc(15){
    vpack if(1){
      all vsbus( 1) {address=0x22; add=TRUE;}
      all vsbus( 2) {address=0x24; add=TRUE;}
      all vsbus( 3) {address=0x26; add=TRUE;}
    }
  }
}
```

FIG. 10

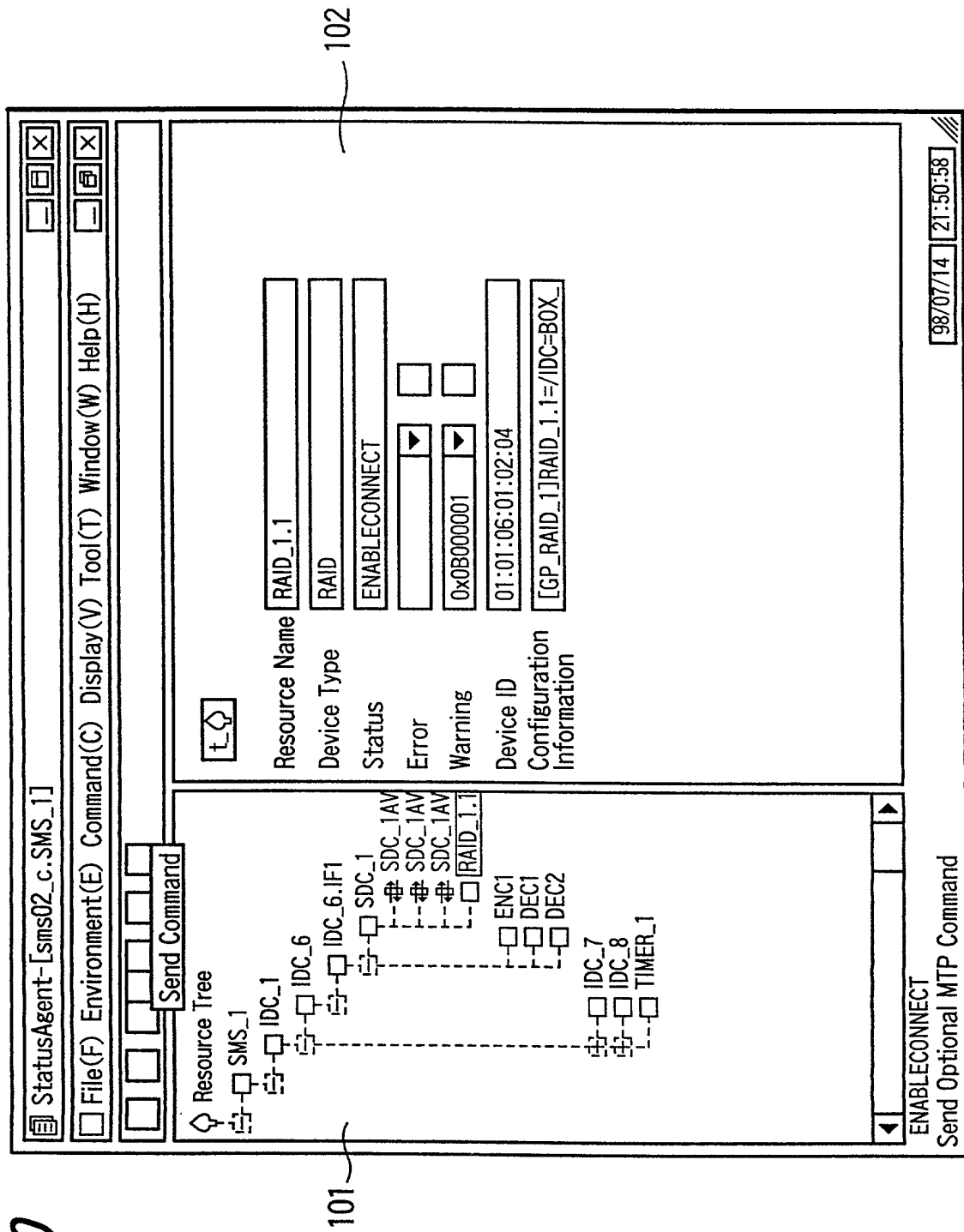


FIG. 11

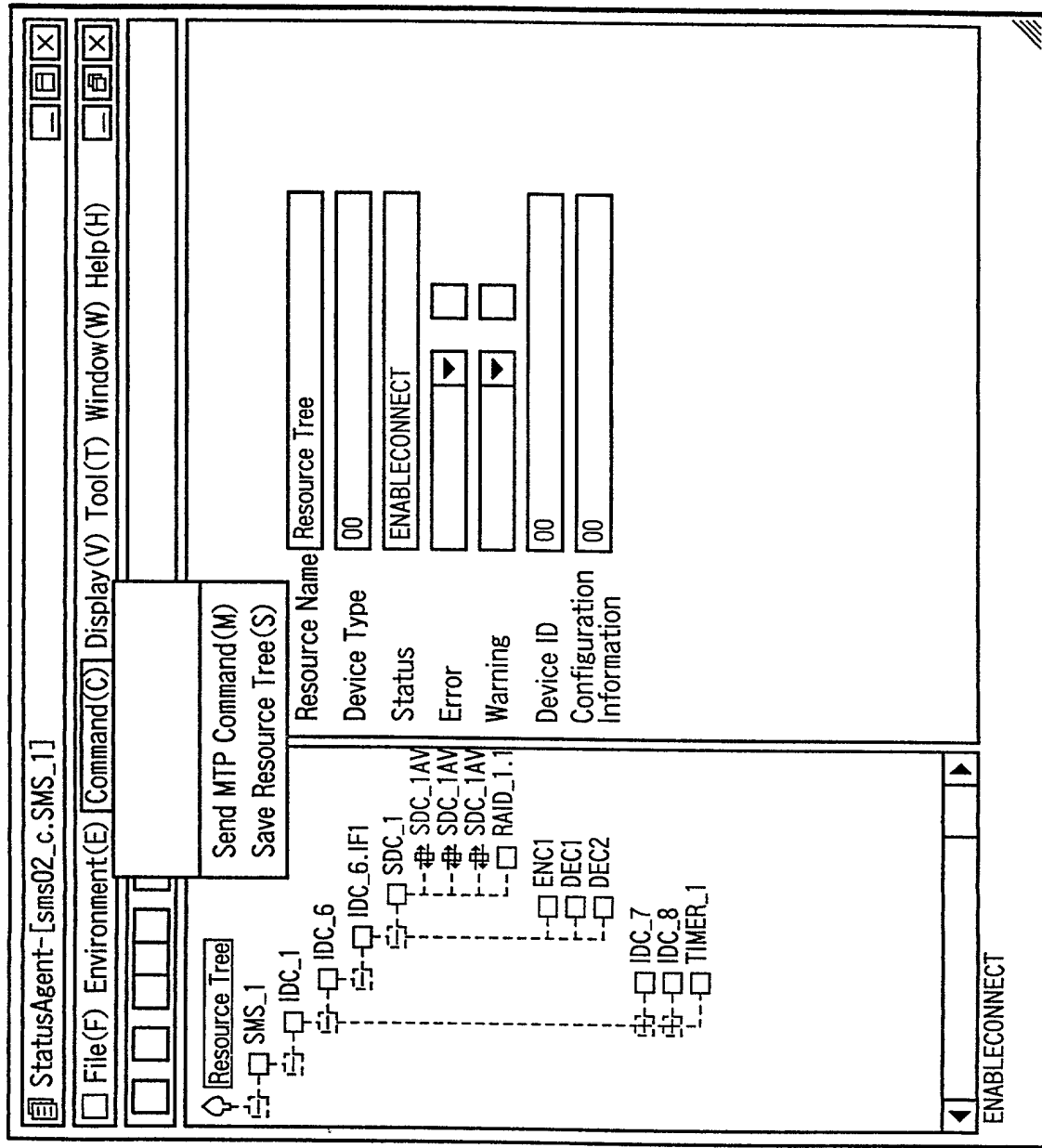
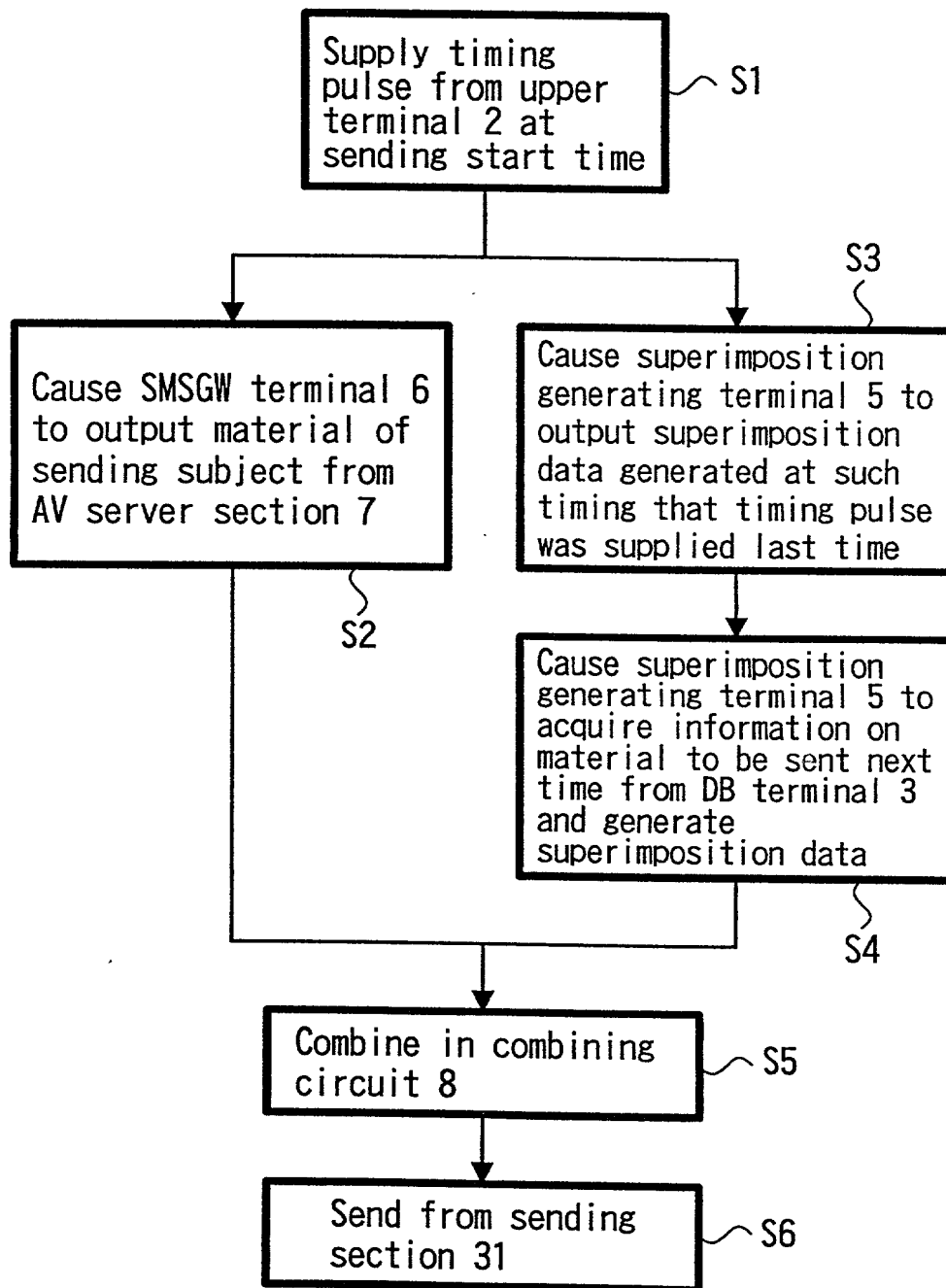


FIG. 12



*FIG. 13*

